Hands-On Space Systems Engineering Education Using Pico-Satellite Training Kit HEPTA-Sat







1U CubeSat Training Kit



Sensor Board

C&DH Board

m

EPS BOARD

CADA SAN

EPS Board

3



Camera

8

Structure

2

Antenna





Concept: "Understand basic satellite system architecture & experience picosatellite development process" Objective: "To gain hands-on experience in the pico-satellite development process with a constrained schedule and acquire basic knowledge of space engineering"



HEPTA-Sat & HEPTA-Sat Training Workshop

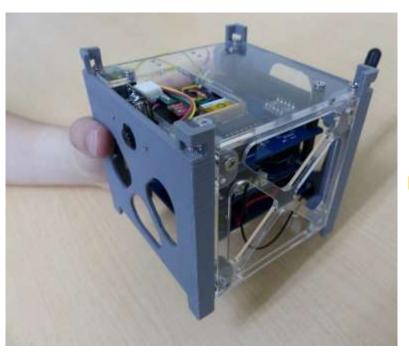
- Target audience of the workshop is anyone who is interested in space.
- We hope HEPTA-Sat will be widely adopted as an opportunity "to understand space engineering" or a tool "to learn pico-satellite engineering."



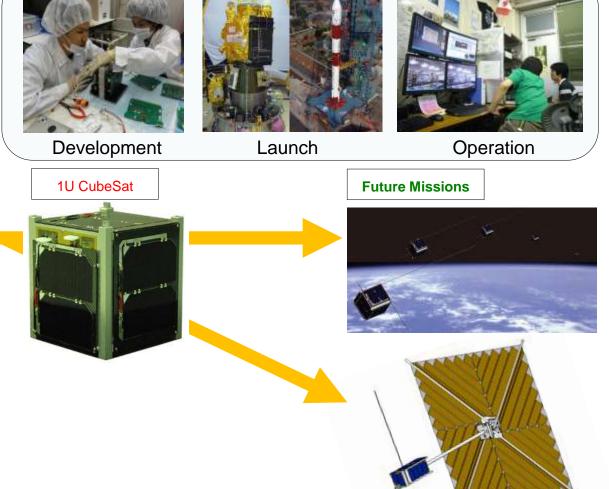
HEPTA-Sat Training Program



Understand basic satellite system architecture
& experience pico-satellite development process.



HEPTA-Sat Training

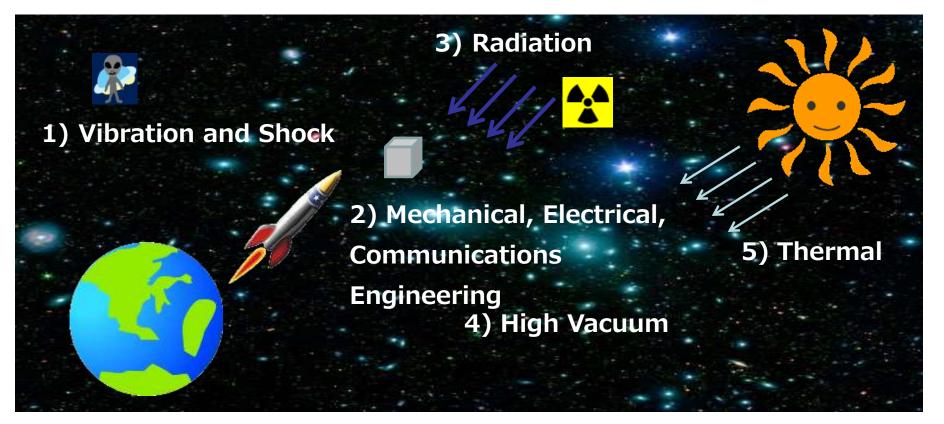




Background



- Satellite projects enable learning of many foundational technologies
 - Mechanical engineering, electrical engineering, communications engineering, and system integration.
 - CubeSat development project based learning is a very effective training method for learning space systems engineering.

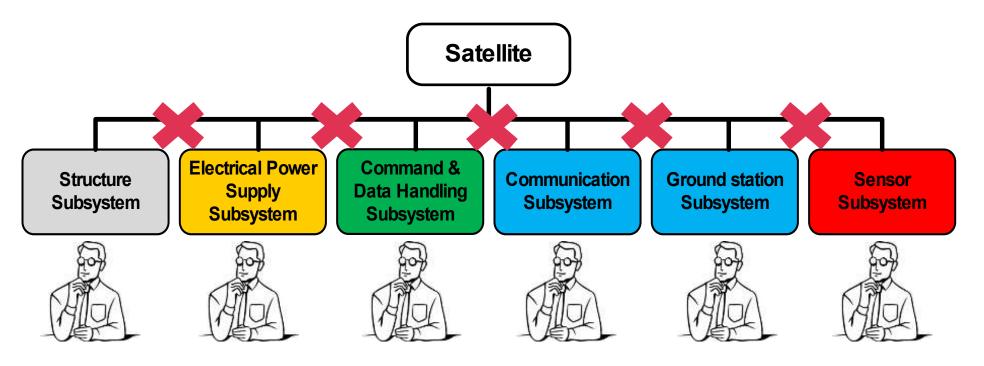




Background



- Satellite projects enable learning of many foundational technologies
 - Mechanical engineering, electrical engineering and communications engineering, and system integration.
 - CubeSat development project based learning is a very effective training method for learning space systems engineering.
- Usually, it is **difficult to gain a breadth of knowledge or experience** across domains because the roles are siloed in separate subsystem teams.

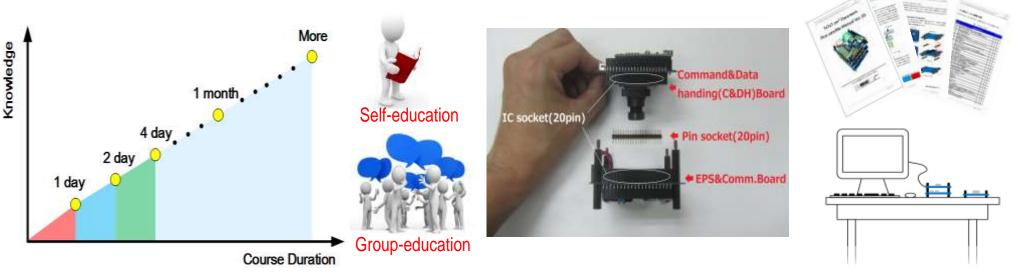






Features

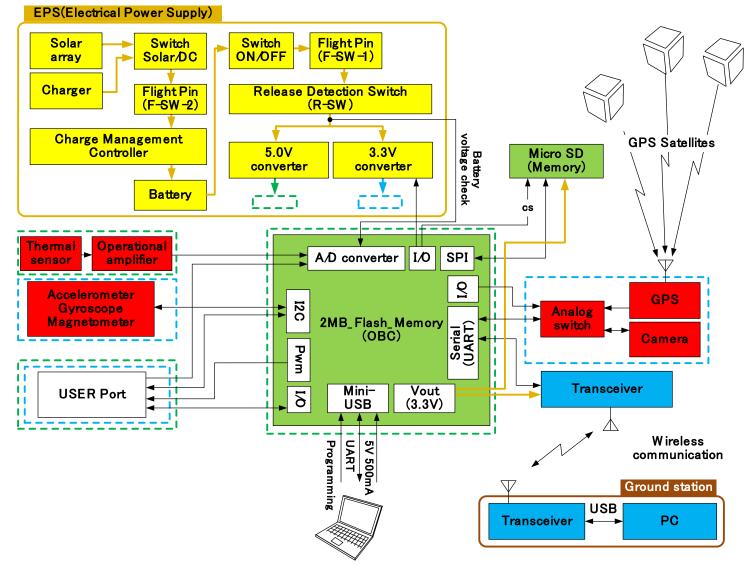
- Effective and low-cost tool that enables space systems engineering education within a short period of time.
- It can be used either by an individual or team.
- Most major components are removable and can be integrated repeatedly.
- Users can design, build, and integrate their own circuit board to run an original mission.
- The textbook allows efficient and systematic self-study of the software, hardware, and pico-satellite, even for a beginner.







Composed of 6 function and 6 primary subsystems.



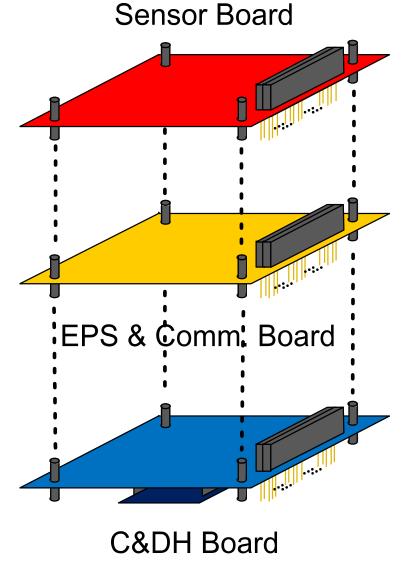




Additional Information







Pin socket

 Electrically connected through pinsockets. Every board has same electrical interface.

Spacer

Physically connected and fixed with spacer.

Electrical Interface

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Upper Surface	GND	VIN	VB	nR	I/0	I/0	I/0	I/0	tx	rx	mosi	miso	sck	rx	I/0	ADC	I/0	I/0	I/0	I/0
Lower Surface	GND	VIN	VB	nR	I/0	I/0	I/0	I/0	tx	rx	mosi	miso	sck	rx	I/0	ADC	I/0	I/0	I/0	I/0
	40	39	38	37	36	35	34	33	3	23	1 30	29	28	27	26	25	24	23	22	21
Upper Surface	VOut	VU	IF-	IF+	RD-	RD+	TD-	TD+	· D·	- D+	+ I/0	I/0	sda	scl	I/0	I/0	I/0	I/0	I/0	I/0
Lower Surface	VOut	VU	IF-	IF+	RD-	RD+	TD-	TD+	- D-	- D+	+ I/O	I/0	sda	scl	I/0	I/0	I/0	I/0	I/0	I/0





• A 40-pin electrical interface between the boards. Each pin has its own role.

OBC Electrical Interface Table											
Pin No. Interface Type			Com	ponent-1	Pin No.	Interfac	e Type	Component-1			
1	GND	0V	-	-	40	Vout	2 21/	VDD	XBee		
					40	vout	3.3V	VDD	SD Card		
2	VIN	5V	Vout	5V Converter	39	Vu	5V	Cathode	Zenerdiode		
3	VB	3.3V	Vout	3.3V Converter	38	—	—	-	-		
4	nR	_	-	_	37	_	_	-	-		
5	mosi		CMD		36	_	_	_	-		
6	miso	SPI	DAT0	SD Card	35	_	_	_	_		
7	sck		CLK	SD Caru	34	_	_	_	-		
8	Digital I/O	0	DAT3		33	_	_	_	_		
9	tx	UART-1	Din	Xbee	32	_	_	_	-		
10	10 rx		Dout	VDee	31	_	_	_	_		
11	mosi	_	_	_	30	Digital I/O	I/O	_	-		
12	miso	_	_	_	29	Digital I/O	I/O	_	_		
13	tx	Serial	RxD	САМ	28	sda		sda	9-axis		
10		OCHAI		UAW		304	I2C	sda	user		
14	rx	Serial	10/I	Analog Switch	27	scl	120	scl	9-axis		
1.7		OCHAI	20/I	Analog Switch	21	301		scl	user		
15	Analog In	Ι	_	_	26	Digital I/O	0	EN	3.3V Regulator		
16	Analog In	Ι	V+	Battery	25	Digital I/O	Ι	2C	Analog Switch		
17	Analog In	Ι	OUTD	OP Amplifer	24	Digital I/O	Ι	1C	Analog Switch		
18	Analog I/O	I/O	I/O	user	23	Digital I/O	Ι	_	-		
19	Analog In	Ι	Ι	user	22	Digital I/O	Ι	pwm	user		
20	Analog In	Ι			21	Digital I/O	Ι	pwm	user		





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