

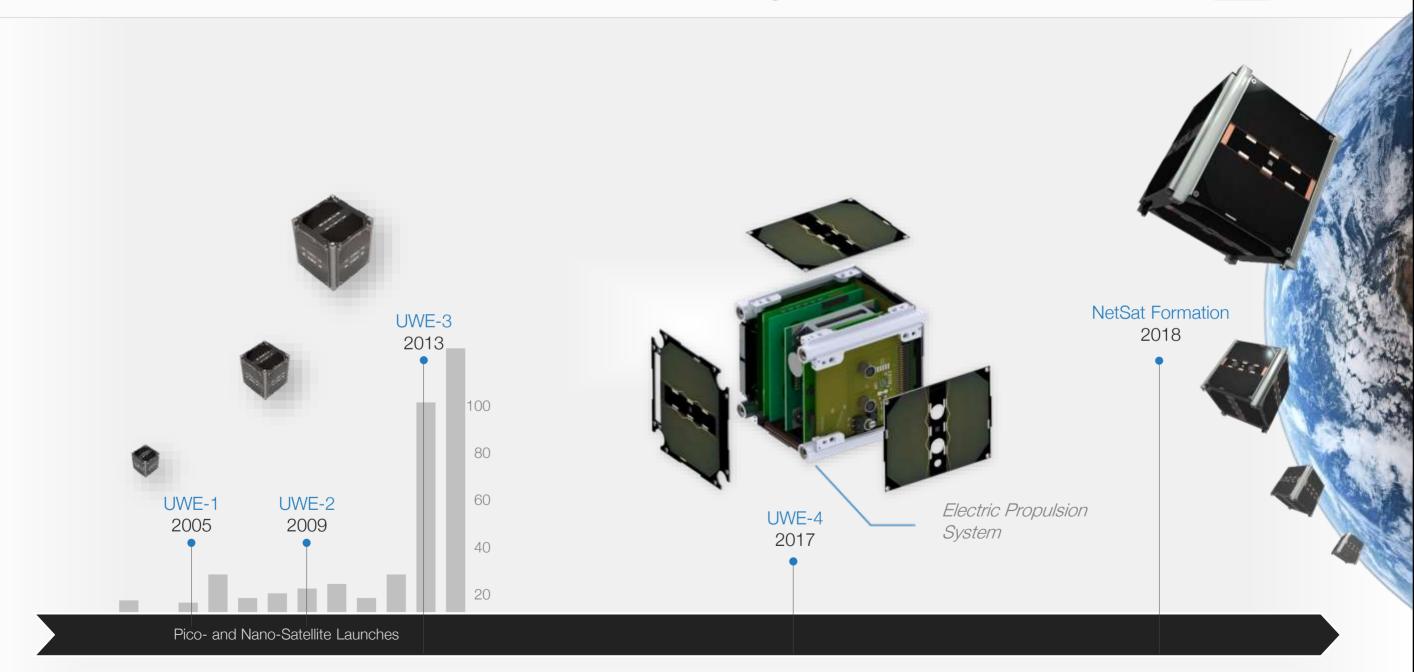
Lessons Learned From More Than 10 Years CubeSat Activities in Wuerzburg, Germany

4th UNISEC Global Meeting October 18-23, Bulgaria Stephan Busch • University of Würzburg • Oct. 2016

ephan Busch

Miniature Satellites from Würzburg (Germany)





Chances and Challenges

Challenges



Performance

• Miniaturization limits available resources

Durability

• Only 40-50% of miniature satellites reach their primary mission objective in orbit

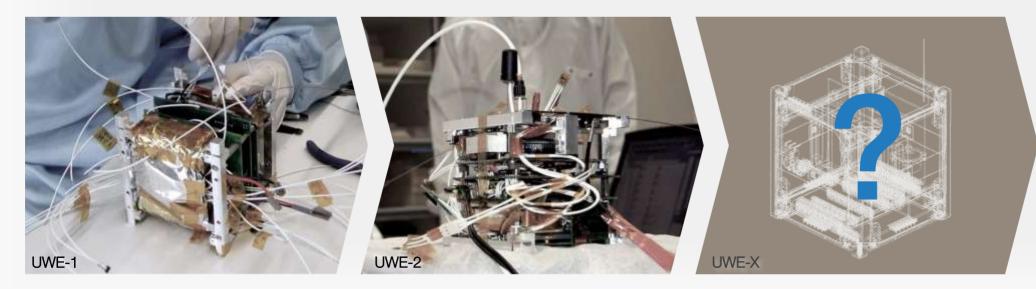
Bouwmeester und Guo, 2010; Swartwout, 2013

Fast Development Cycle

• Design iteration und launch within a few months: How to assure quality?

Modern Production

• Production, Integration, and Test: from High-tech Manufactures to Batch Production



Challenging Trade-Off



performance optimization despite limited resources

Efficiency Robis Flexibility reusable, reliable scalable, and and failure-tolerant extendable

Testability as Design Driver



Access to Hardware

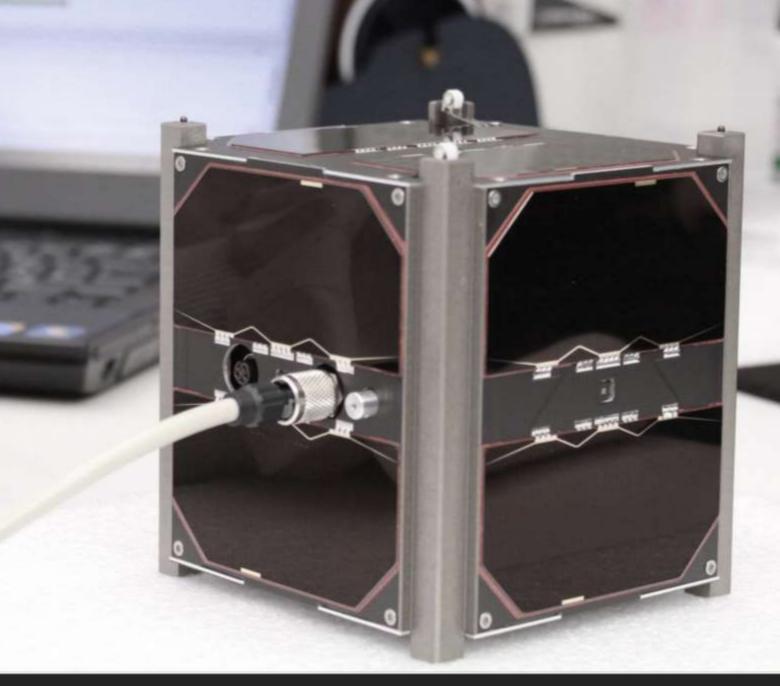
 fast and simple functional integration tests

Access to Software

- embedded Unit tests
- generic parameter access

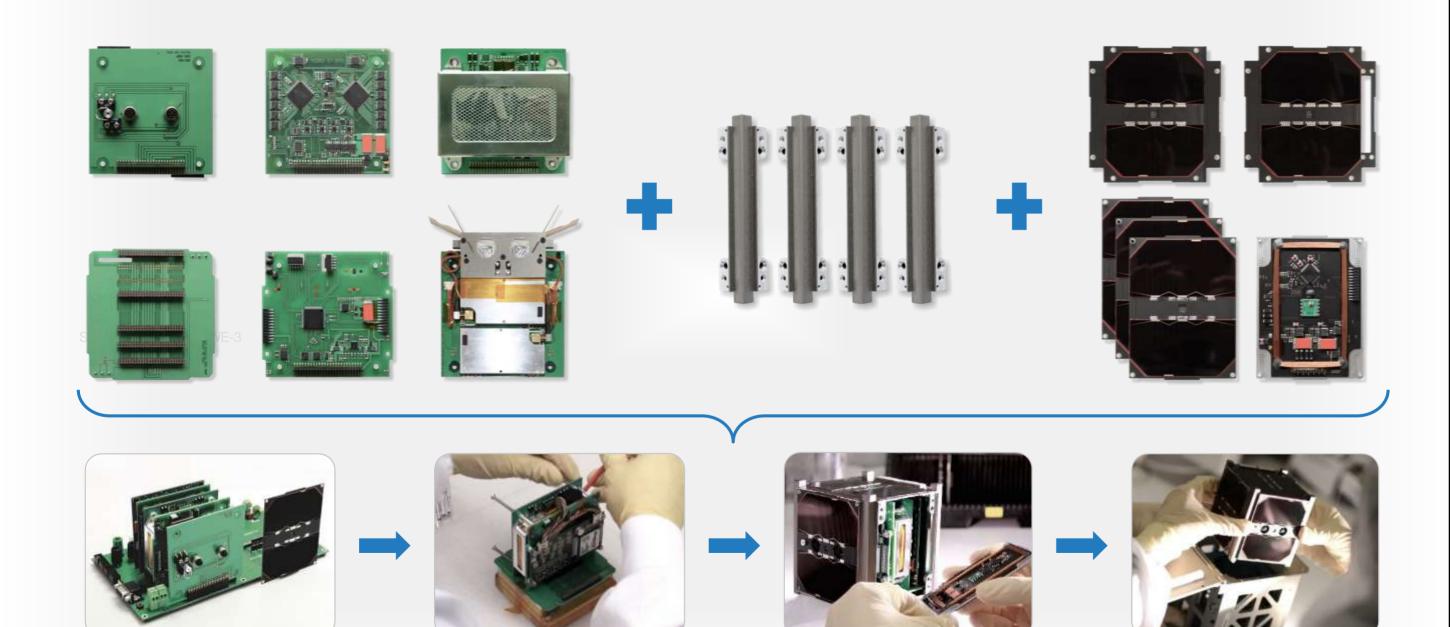
Access to Test Equipment

 simple test setups for continuous automated verification



Access to Hardware





Testability as Design Driver



Access to Hardware

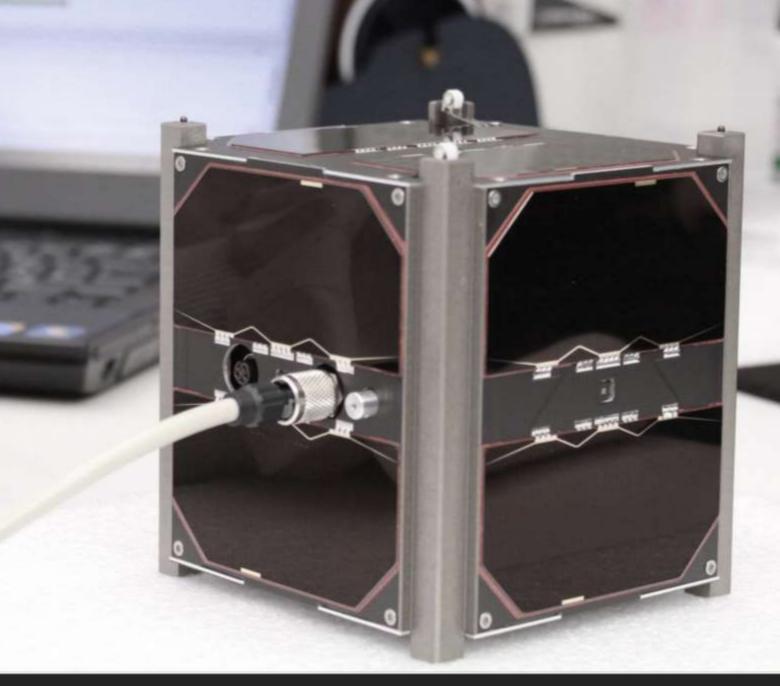
 fast and simple functional integration tests

Access to Software

- embedded Unit tests
- generic parameter access

Access to Test Equipment

 simple test setups for continuous automated verification



Access to Software



Embedded Unit Testing

- simple, rapid to use
- continuous check of each single feature
- immediate detection of potential failures

Wekowe 🖸 Operation						
P Debug Logging View	11 L		C Unit Texts		2 3 8	
Time	Level	Message	Test Nome	(II)	Status	
30,09,2009 23,27,38	INFO	Version Compled: 5ep 25 2009 - 10:42:04 (300	🕀 🗖 Elink Tests	0	The second secon	
30.09.2009 23:27:30 30.09.2009 23:26:19	WARNING INPO	protocol: transfer error: PACKETLOST(1.614,0: Version Compiled: Sep 25 2009 - 10:42:04 (300	Pal Test	1	And it was a finding a new first of \$2.4 (1) (2/3)	
			Success Test	12		
			😑 🗖 Plash Protection	3		
			Replicate LR	4		
			Replicate LC	5		
			E 🖸 Local Protect	6	Local Protect: success	
			Error in LP	7	Check for carried LC: soccess (9)(9)	
			Error in LR	8	check for connect LC. soccess (9/9)	
			Error in LC	9	ched for correct LC: success (9)9)	
			Fundet	1.0		
			B D JTAG	-11		
			PSA PSA	12		
			⊡ Local Plash	13		
	-	4.2	Large Memory Model	14		
	1000		😑 🗹 Local Flash Write (from Flash)	15	pending	
-			Local Flash Write [IM]	16	ment copi and ope checks a costs (US)	
State of the local division of the local div			Local Flash Write [5]	17	new over and wire the purcess (65/128)	
1			Cocal Flash Write [C]	£8	pending	
60			Local Flash Write [D]	19	pending	
		1 1	E Local Resh Write [from RAM]	20		
	+	<u> </u>	Local Flash Write (B)	21		
		Send	Unit Tests Log	1.000		

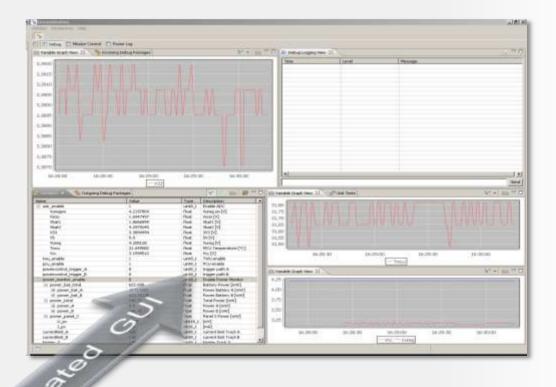
```
EUNIT_DEFINE(test, funclettest, "Funclet", 2, EUNIT_ROOT)
uint8_t code[sizeof(Funclet_o)];
//check location
address_t addr;
addr.u8_ptr = (uint8_t*)Funclet_o;
EASSERT("funclet link location", addr.u32 > Ox5BFF, "
```

Access to Software



Embedded Variable Synchronization

- simple, rapid to use one-line coding effort
- read/write sync
- automated GUI and data export



VARSYNC_DEFINE(var, U_B, uint16_t, "[mV]", "", (void)0. (void*)0, \$power_B) VARSYNC_DEFINE(var, I_B, int16_t, "[mA]", "", (void)0. (void*)0, \$power_B)

VARSYNC_DEFINE(var, power_panel_X, float, "Panel X Power [mW]", "", (void*)0, (void*)(VARSYNC_DEFINE(var, U_px, uint16_t, "[mV]", "", (void*)0, (void*)0, \$power_panel_X) VARSYNC_DEFINE(var, I_px, int16_t, "[mA]", "", (void*)0, (void*)0, \$power_panel_X)

Testability as Design Driver



Access to Hardware

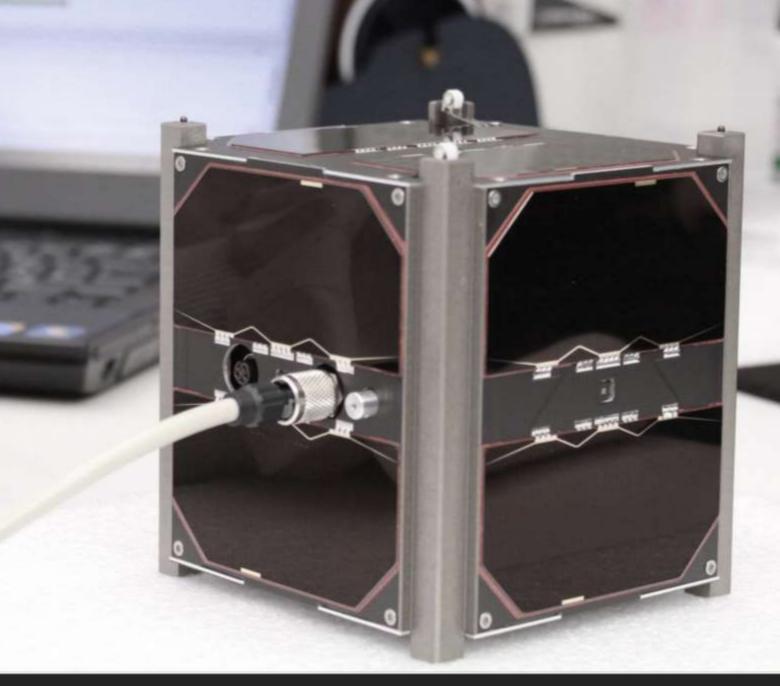
 fast and simple functional integration tests

Access to Software

- embedded Unit tests
- generic parameter access

Access to Test Equipment

 simple test setups for continuous automated verification

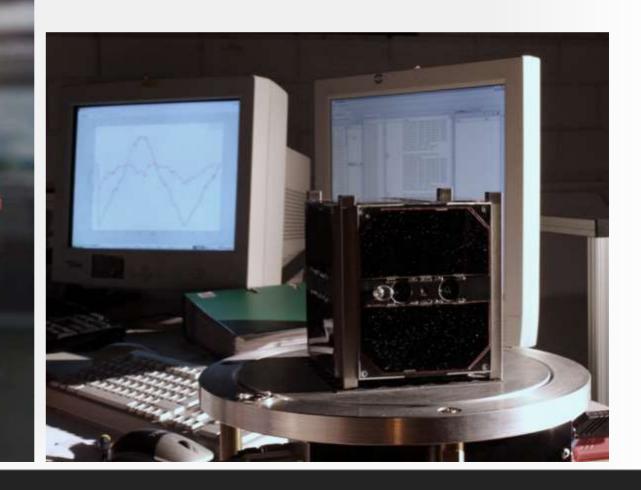


Access to Test Equipment



"...when you don't have access to traditional test facilities, then be creative!"

Mengu Cho (2016)

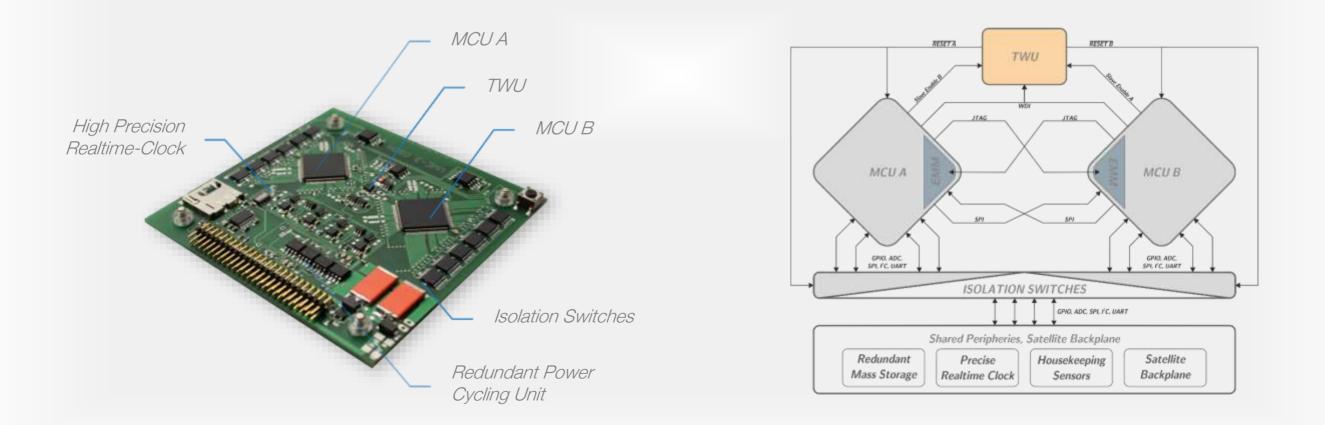




... be lean, but don't forget to be robust...

Robust and Energy Efficient OBDH Core Module

- Optimized as dedicated housekeeping und autonomous FDIR module
- 2 redundant Microcontroller (MCU) in *warm-backup*
- Less than 10 mW total power consumption





Launch





UWE-3 Launch – 21. Nov. 2013

- Yasny Launch Base, Russia
- Cluster Launch with 29 satellites, a.o.
 - DubaiSat-2
 - SkySat-1
 - Dove-3/4



Image credits: Kosmotras

Launch



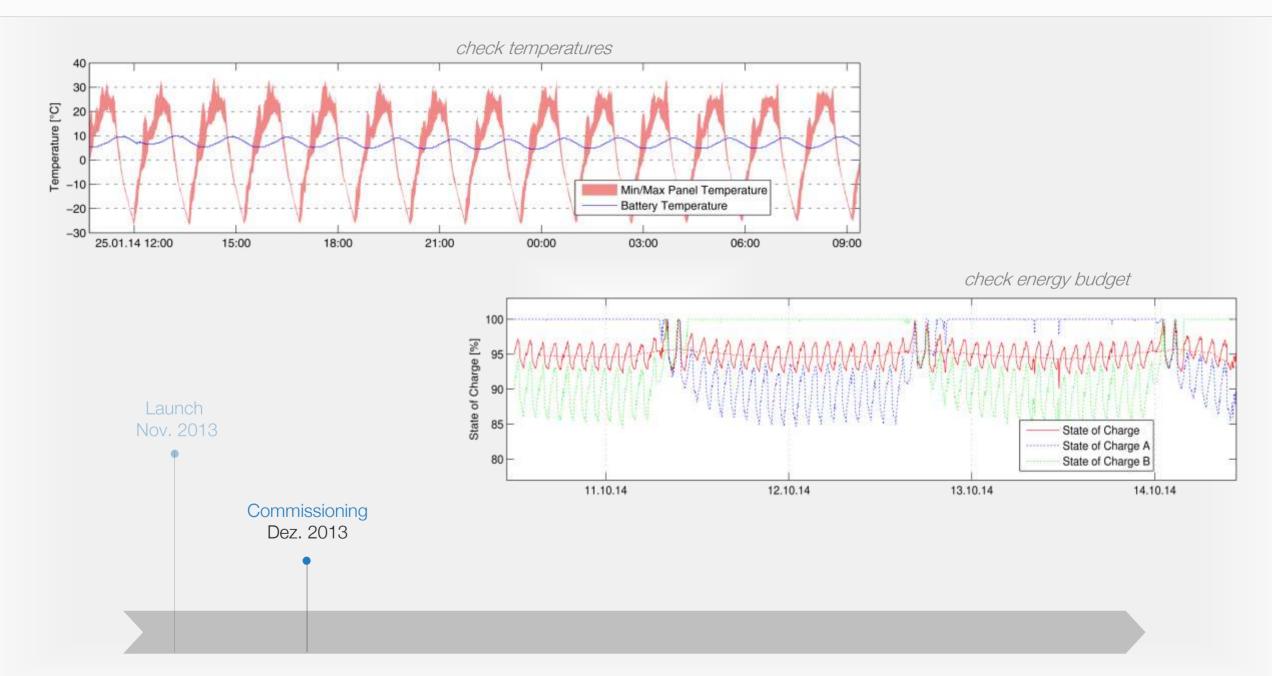


Image credits: Kosmotras

UWE-3 Launch – 21. Nov. 2013

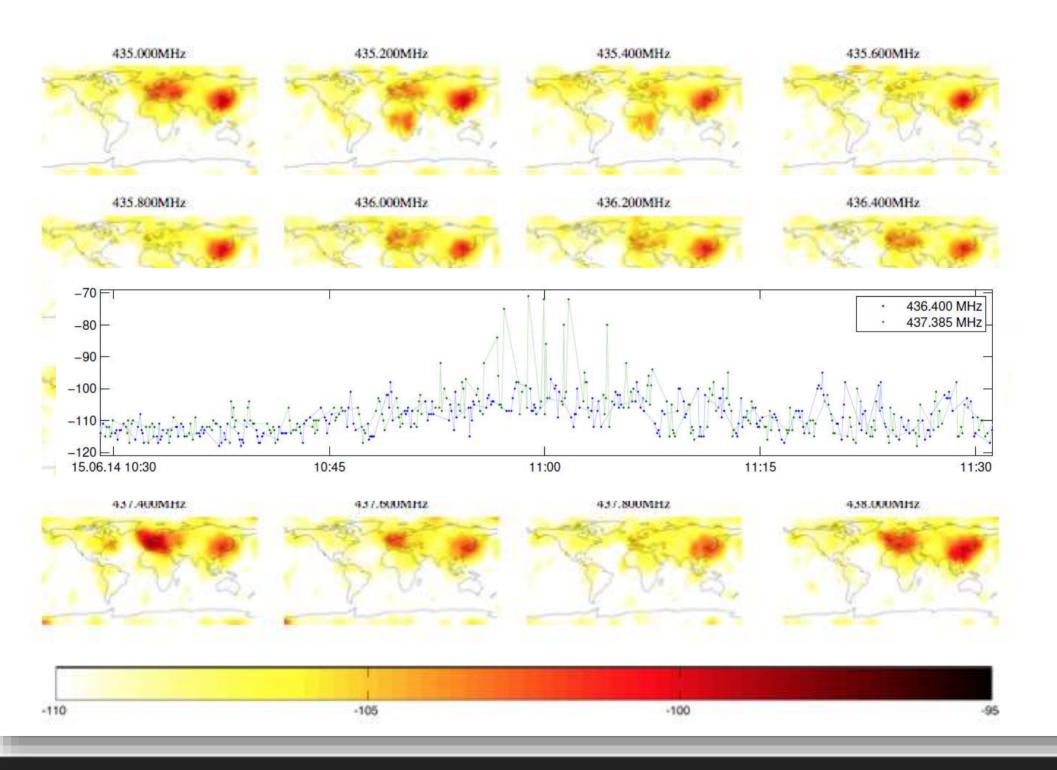
 Integration into Space Head Module of a decommissioned
 Dnepr intercontinental rocket

...don't forget what happens after launch...



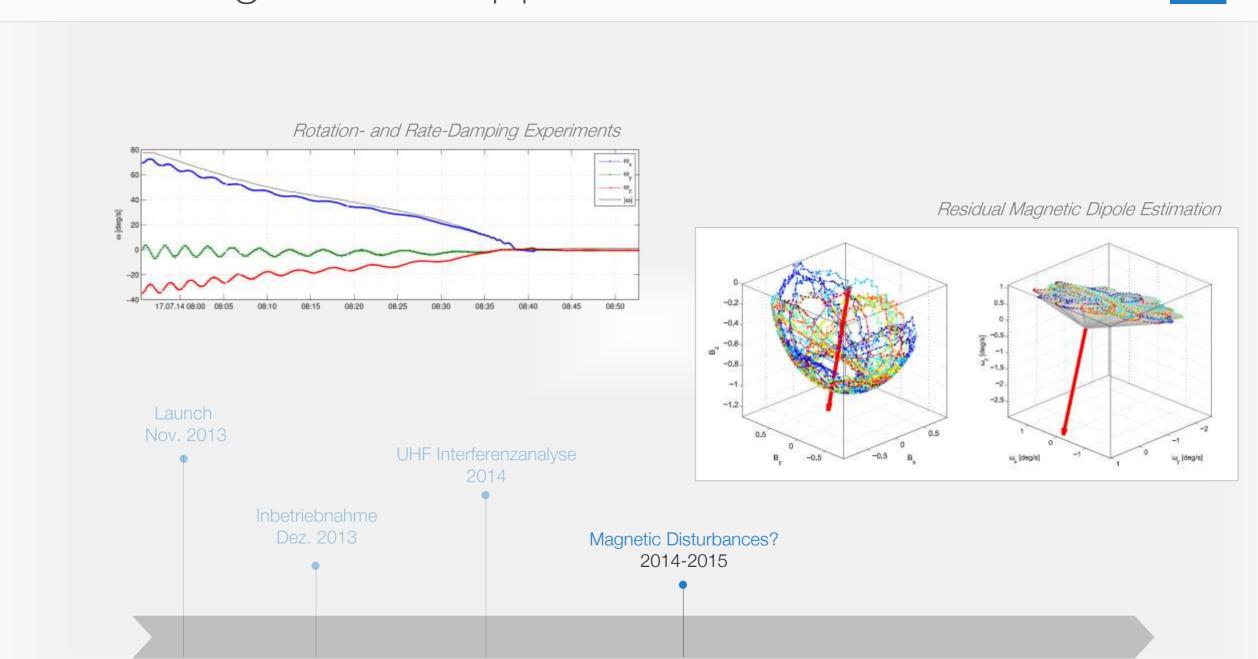
ulius-Maximilians-

UNIVERSITÄT WÜRZBURG ...d(





...don't forget what happens after launch...



Launch und Betrieb – Ausblick

ulius-Maximilians-

UNIVERSITÄT WÜRZBURG

...don't think there are no challenges left...



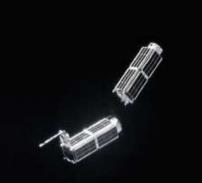


Mega-Constellations and Formations of Small Satellites

- Earth Observation
- Communication

Examples

- OneWeb: 648
- SpaceX: 4024
- PlanetLabs: ~100





Chances and Challenges

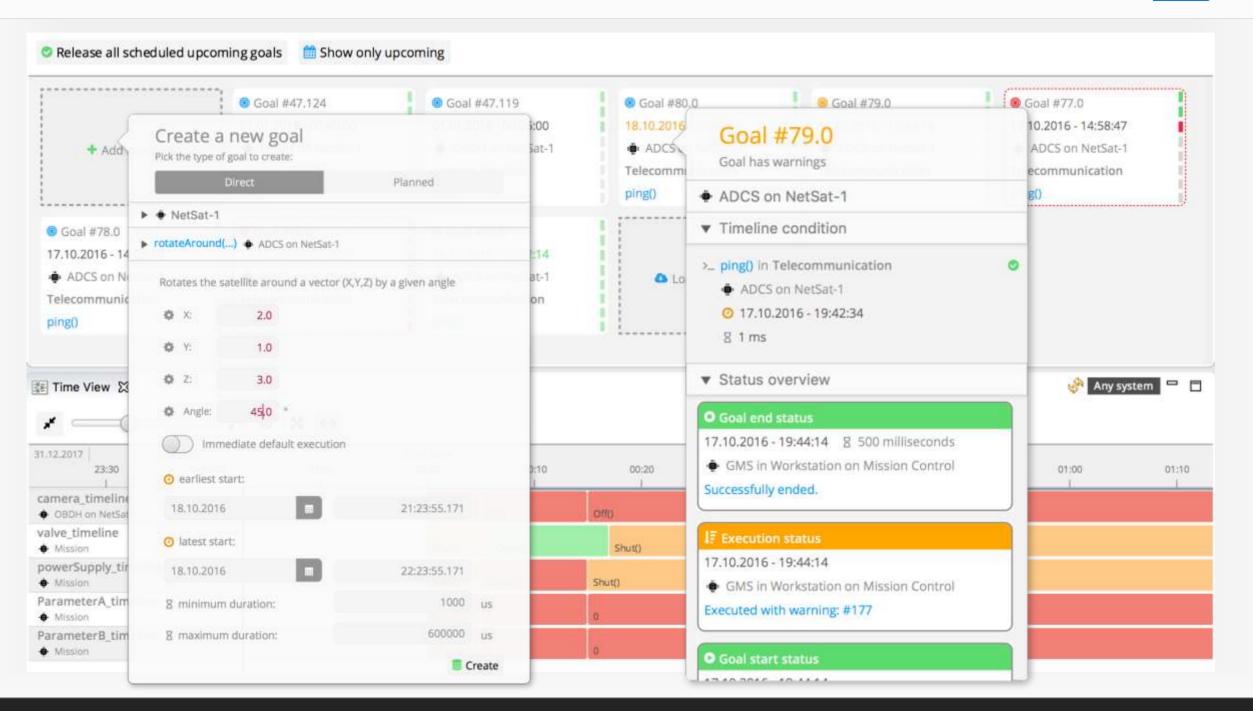
UNISEC Global Meeting - 18-23 Oct. - Bulgaria

Dr. Stephan Busch

Image credits: NASA. PlanetLabs

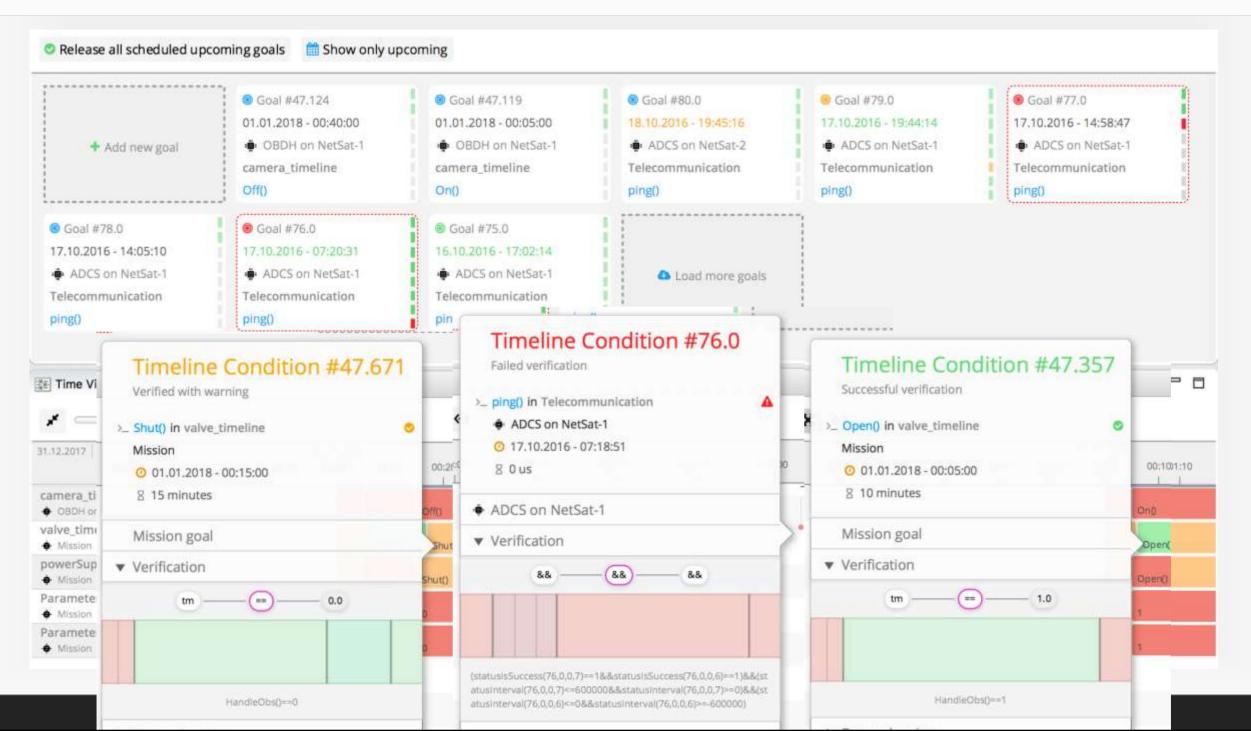
How to build, test, and operate hundreds of satellites?





how to build, test, and operate hundreds of satellites





Thank You!



